

## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In Re Application of	)	<b>CALIBRATING AN INTEGRATED</b>
<b>Jagrut V. Patel</b>	)	<b>For: CIRCUIT TO AN ELECTRONIC</b>
	)	<b>DEVICE</b>
	)	
<b>Serial No. 10/722,350</b>	)	<b>Examiner: Fahmida Rahman</b>
	)	
<b>Filed: November 25, 2003</b>	)	<b>Group No. 2116</b>
	)	
	)	<b>Confirm No. 4737</b>

**AMENDMENT AND RESPONSE TO OFFICE ACTION**

Mail Stop Amendment  
 Commissioner for Patents  
 P.O. Box 1450  
 Alexandria, VA 22313-1450

Dear Sir:

In response to the Office Action dated March 21, 2007, please amend the above-identified application as indicated below.

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**CERTIFICATE OF MAILING/TRANSMISSION (37 CFR 1.8(a))**

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Date: September 21, 2007

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